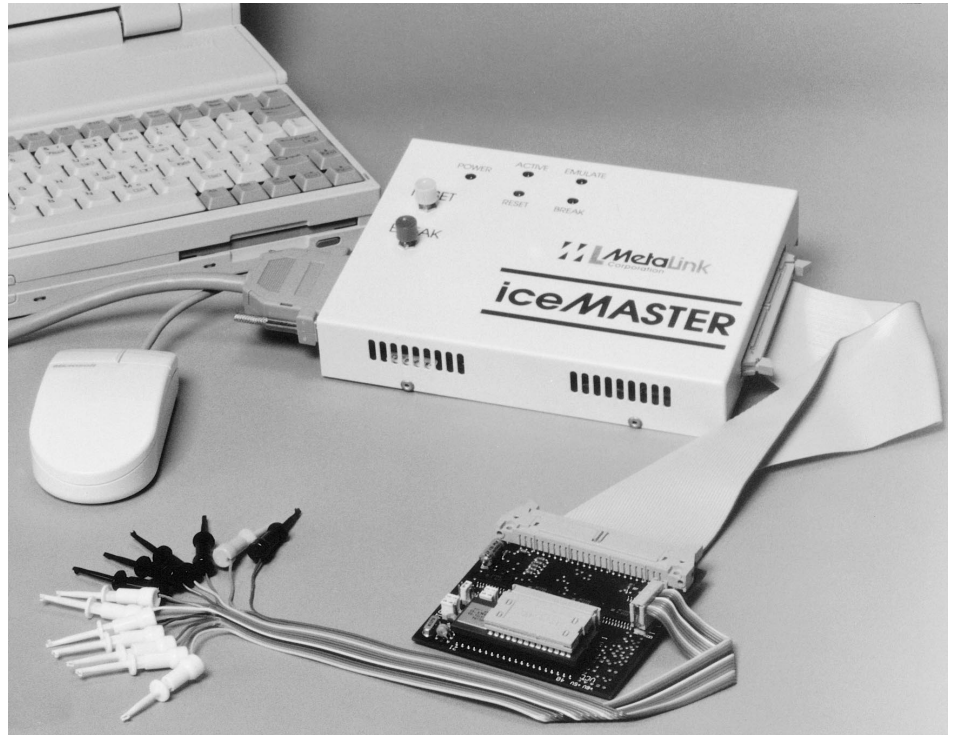


iceMASTER-AA

- PC-Hosted Via RS-232 Serial Link
- Interchangeable Probe Cards
- 64K Code and 64K External Data Memory
- 4K Frame Trace Buffer (Optional)
- 128K Hardware Breakpoints
- 64K Trace On/Off Triggers
- Supports Third-Party Assemblers & Compilers
- Full Symbolic & Source-Level Debug
- Built-In Low Cost Logic Analyzer (Optional)
- Built-In Performance Analyzer (Optional)

The iceMASTER-AA is a modular emulator for the Intel 8051 family which consists of an emulator base unit which uses interchangeable probe cards. Various Intel microcontrollers are supported by selecting the appropriate probe card for the particular device. This emulator can plug directly into a target application or operate in stand-alone mode. A 4K frame trace buffer is available which captures data in real-time. Trace information consists of the code address, external data values and logic values from external probe clips. The trace buffer can be triggered to capture data on all instructions leading up to a breakpoint, around (before and after) a breakpoint, or following a breakpoint. Trace ON/OFF points are also available to filter data before storing in the trace buffer, allowing the user to focus only on areas of interest.

To improve the chances of finding a software bug, 2 categories of breakpoints are available to the user. The first is a simple breakpoint which is based on a code or external data address. The second type is a complex breakpoint which is based on a code address, direct address, bit address, opcode values, opcode class or immediate operand. Complex breakpoints can also be ANDed and ORed together to provide a more efficient debugging session.



All iceMASTER emulators are controlled from a windowed user interface where commands can be accessed from pull-down menus or through redefinable hot keys. Full source-level debugging is available for most Assemblers, C-compilers and PL/M compilers. Once loaded into the emulator, the user can view code memory in assembly language, source code for 'C' or PL/M and a mixed mode where the assembly language instructions are shown directly below each high-level source code instruction.

MICROCONTROLLERS SUPPORTED:

8xC3x, 8xC5xFx, 8xC5x, 8xC152Jx

DEVELOPMENT PLATFORMS:

PC (Non-Windows)

AVAILABILITY:

Now

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For international contacts, see Appendix B.